

**TO-252 N Channel Enhancement 沟道增强型
MOS Field Effect Transistor 场效应管**

■Features 特点

Low on-resistance 低导通电阻

$R_{DS(ON)}=9.2\text{m}\Omega$ (Type)@ $V_{GS}=10\text{V}$

$R_{DS(ON)}=15\text{m}\Omega$ (Type)@ $V_{GS}=4.5\text{V}$

■Applications 应用

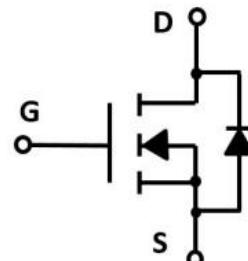
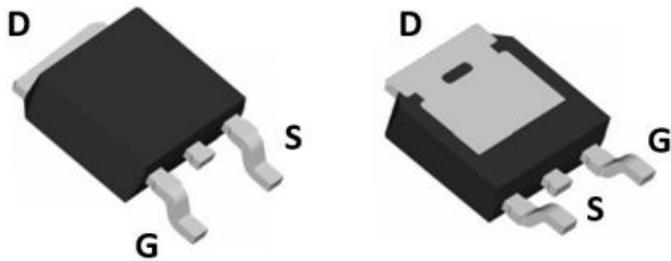
Load switch 负载开关

DC/DC Converter 升压转换

Power Management 电源管理



■Internal Schematic Diagram 内部结构



■Absolute Maximum Ratings 最大额定值

Characteristic 特性参数	Symbol 符号	Rating 额定值	Unit 单位
Drain-Source Voltage 漏极-源极电压	BV_{DSS}	30	V
Gate- Source Voltage 栅极-源极电压	V_{GS}	± 20	V
Drain Current (continuous)漏极电流-连续	I_D	30	A
Drain Current (pulsed)漏极电流-脉冲	I_{DM}	100	A
Total Device Dissipation 总耗散功率	P_D (at $T_A = 25^\circ\text{C}$ at $T_C = 25^\circ\text{C}$)	3.6 40	W
Thermal Resistance Junction-Ambient/Case 热阻	$R_{\theta JA}/R_{\theta JC}$	55/3.8	°C/W
Junction/Storage Temperature 结温/储存温度	T_J, T_{stg}	-55~150	°C

■ Electrical Characteristics 电特性

($T_A=25^\circ\text{C}$ unless otherwise noted 如无特殊说明, 温度为 25°C)

Characteristic 特性参数	Symbol 符号	Min 最小值	Typ 典型值	Max 最大值	Unit 单位
Drain-Source Breakdown Voltage 漏极-源极击穿电压($I_D=250\mu\text{A}, V_{GS}=0\text{V}$)	BV_{DSS}	30	—	—	V
Gate Threshold Voltage 栅极开启电压($I_D=250\mu\text{A}, V_{GS}=V_{DS}$)	$V_{GS(\text{th})}$	1	1.6	2.5	V
Zero Gate Voltage Drain Current 零栅压漏极电流($V_{GS}=0\text{V}, V_{DS}=30\text{V}$)	I_{DSS}	—	—	1	μA
Gate Body Leakage 栅极漏电流($V_{GS}=\pm20\text{V}, V_{DS}=0\text{V}$)	I_{GSS}	—	—	±100	nA
Static Drain-Source On-State Resistance 静态漏源导通电阻($I_D=10\text{A}, V_{GS}=10\text{V}$) ($I_D=5\text{A}, V_{GS}=4.5\text{V}$)	$R_{DS(\text{ON})}$	—	9.2 15	12 21	$\text{m}\Omega$
Diode Forward Voltage Drop 内附二极管正向压降($I_{SD}=20\text{A}, V_{GS}=0\text{V}$)	V_{SD}	—	—	1.2	V
Input Capacitance 输入电容 ($V_{GS}=0\text{V}, V_{DS}=15\text{V}, f=1\text{MHz}$)	C_{ISS}	—	938	—	pF
Common Source Output Capacitance 共源输出电容($V_{GS}=0\text{V}, V_{DS}=15\text{V}, f=1\text{MHz}$)	C_{OSS}	—	142	—	pF
Reverse Transfer Capacitance 反馈电容 ($V_{GS}=0\text{V}, V_{DS}=15\text{V}, f=1\text{MHz}$)	C_{RSS}	—	99	—	pF
Total Gate Charge 栅极电荷密度 ($V_{DS}=15\text{V}, I_D=20\text{A}, V_{GS}=10\text{V}$)	Q_g	—	17.5	—	nC
Gate Source Charge 栅源电荷密度 ($V_{DS}=15\text{V}, I_D=20\text{A}, V_{GS}=10\text{V}$)	Q_{gs}	—	3	—	nC
Gate Drain Charge 栅漏电荷密度 ($V_{DS}=15\text{V}, I_D=20\text{A}, V_{GS}=10\text{V}$)	Q_{gd}	—	4.1	—	nC
Turn-ON Delay Time 开启延迟时间 ($V_{DS}=15\text{V} I_D=20\text{A}, R_{\text{GEN}}=3\ \Omega, V_{GS}=4.5\text{V}$)	$t_{d(\text{on})}$	—	5	—	ns
Turn-ON Rise Time 开启上升时间 ($V_{DS}=15\text{V} I_D=20\text{A}, R_{\text{GEN}}=3\ \Omega, V_{GS}=4.5\text{V}$)	t_r	—	12	—	ns
Turn-OFF Delay Time 关断延迟时间 ($V_{DS}=15\text{V} I_D=20\text{A}, R_{\text{GEN}}=3\ \Omega, V_{GS}=4.5\text{V}$)	$t_{d(\text{off})}$	—	19	—	ns
Turn-OFF Fall Time 关断下降时间 ($V_{DS}=15\text{V} I_D=20\text{A}, R_{\text{GEN}}=3\ \Omega, V_{GS}=4.5\text{V}$)	t_f	—	6	—	ns

■ Typical Characteristic Curve 典型特性曲线

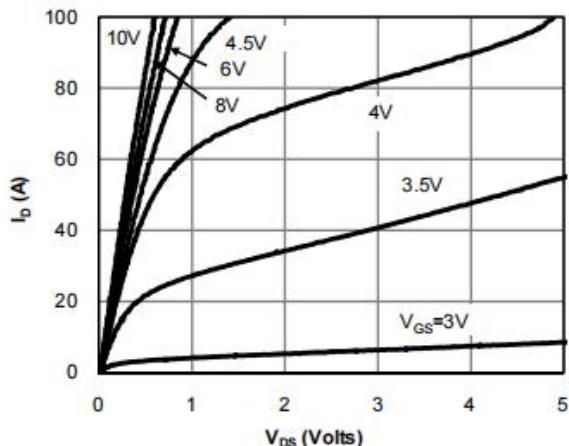


Figure 1: Output Characteristics

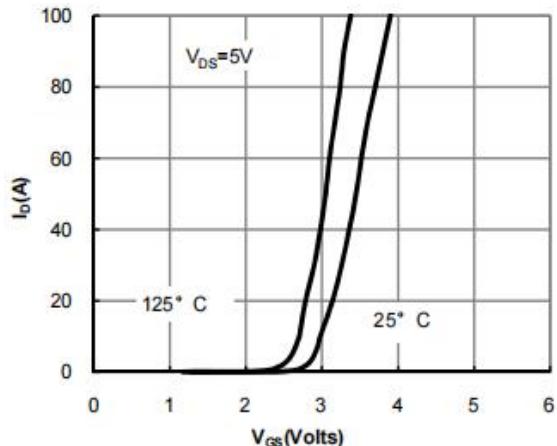


Figure 2: Transfer Characteristics

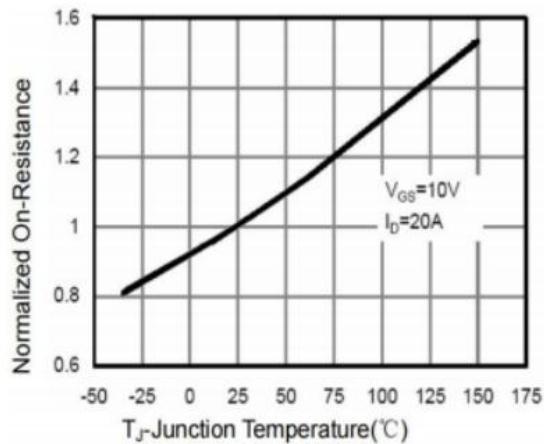


Figure 3: On-Resistance vs. T_J

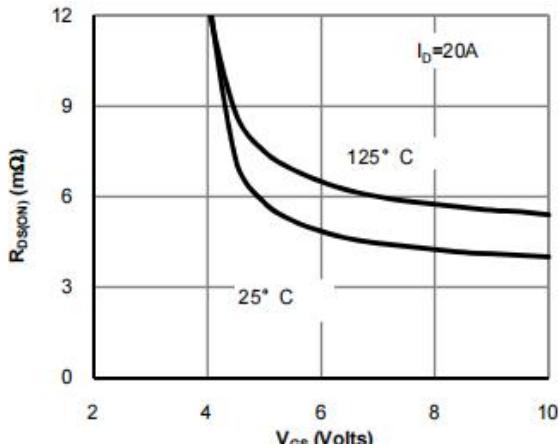


Figure 4: On-Resistance vs. V_{GS}

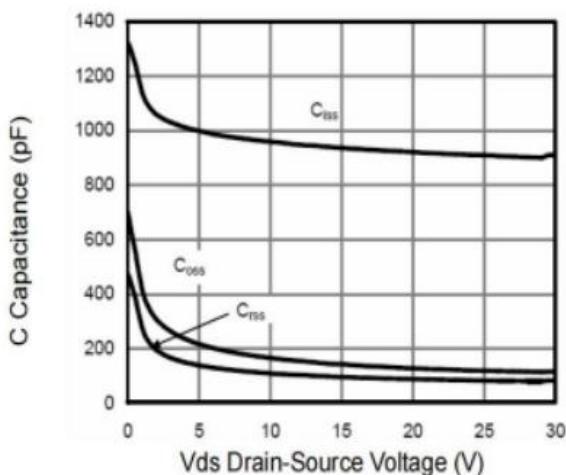


Figure 5: Capacitance Characteristics

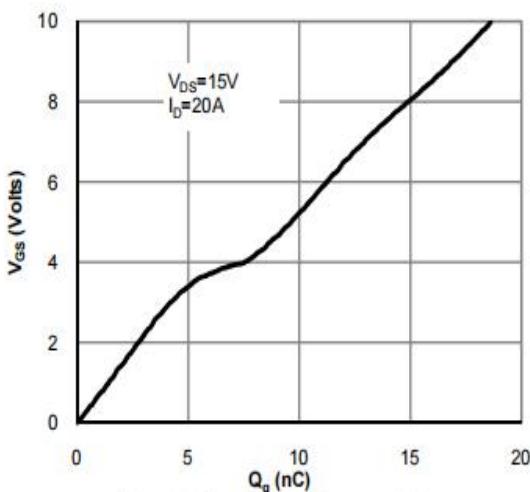


Figure 6: Gate-Charge Characteristics

■Typical Characteristic Curve 典型特性曲线

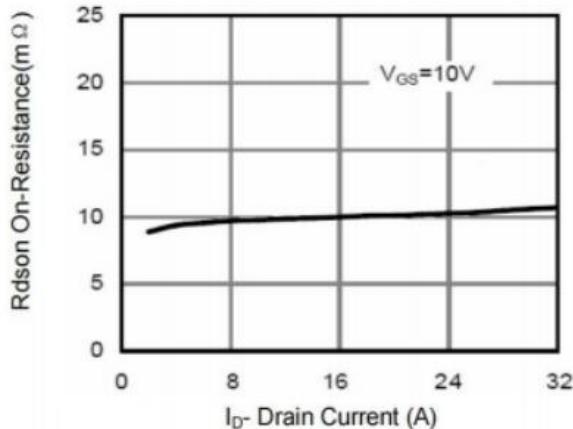


Figure 7: On-Resistance vs. Drain Current

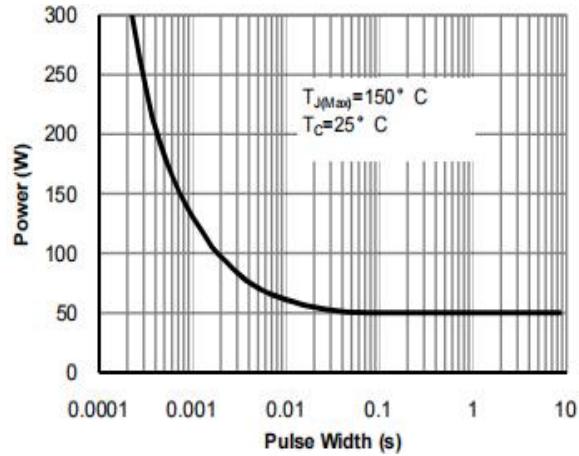


Figure 8: Power Rating Characteristics

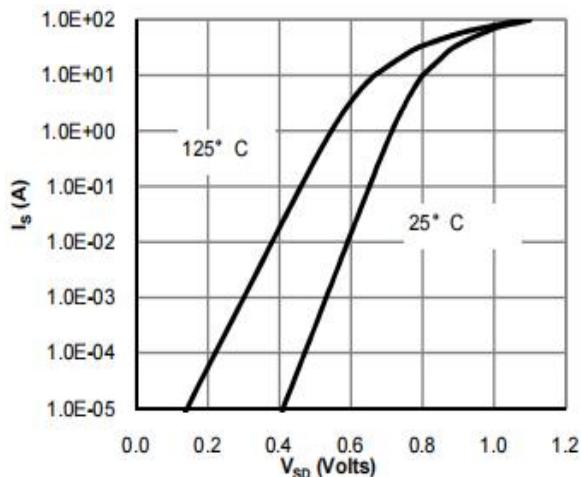


Figure 9: Diode Characteristics

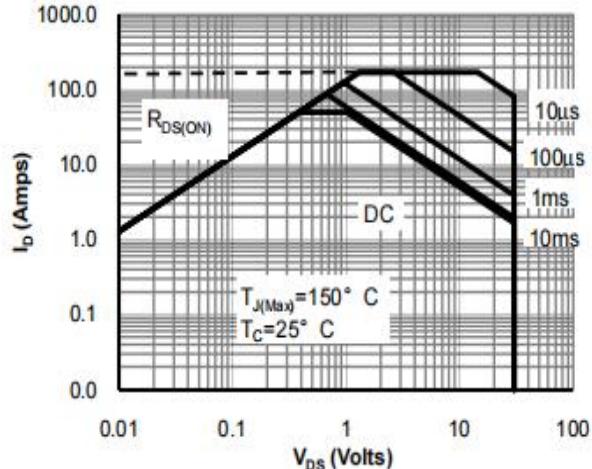


Figure 10: Safe Operating Area

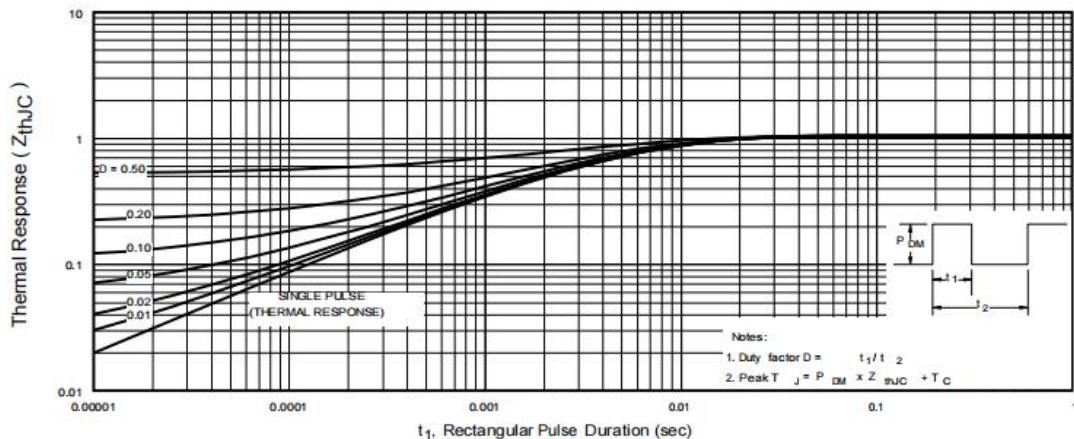
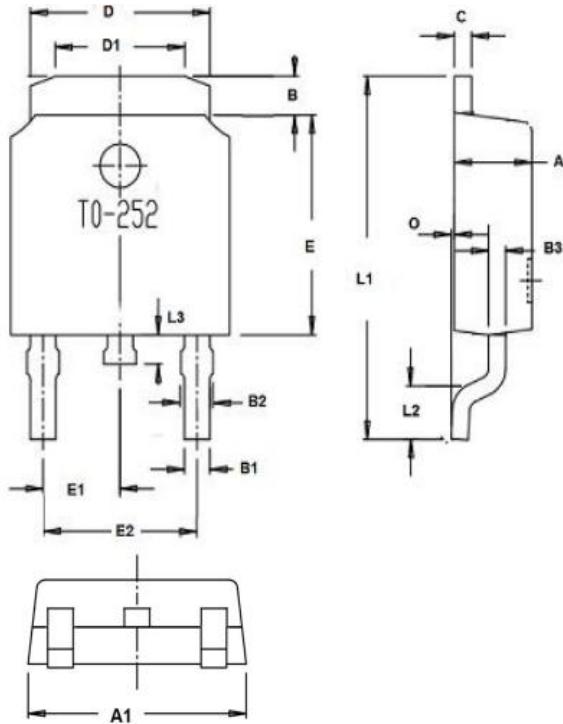


Figure 11: Transient Thermal Response Curve

■ Package Dimension 外形封装尺寸



Dim.	Min.	Max.
A	2.1	2.5
A1	6.3	6.9
B	0.95	1.55
B1	0.6	0.8
B2	0.75	0.95
C	Typ0.5	
D	5.3	5.5
D1	3.65	4.05
E	5.8	6.4
E1	Typ2.3	
E2	Typ4.6	
O	0	0.15
L1	9	11
L2	Typ1.5	
L3	0.7	1
All Dimensions in millimeter		